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## Patent Record View

Monday, October 26 2009

THOMSON INNOVATION

**Patent/Publication:** JP11074863A METHOD FOR RECEIVING MULTIPLE CARRIER WAVE DIGITAL SIGNAL AND RECEIVER**Bibliography****DWPI Title**

Reception method for multi-carrier digital signals subjecting received digital signal to coarse time synchronisation and correlating signals with time-shifted versions of itself for identifying transmission mode

**Original Title**

METHOD FOR RECEIVING MULTIPLE CARRIER WAVE DIGITAL SIGNAL AND RECEIVER

**Assignee/Applicant**

Standardized: THOMSON BRANDT GMBH

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EP1997112929A / 1997-07-28 / EP

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**Abstract****Abstract**

PROBLEM TO BE SOLVED: To improve signal identification, and to reduce erroneous decoding by correlating a digital signal with a deviated digital signal according to various time corresponding to a possible mode in a time area, deciding the present mode according to the maximum position and size of the correlation value, and obtaining an information item related with rough time synchronization and a present symbol from an output signal.

SOLUTION: An input signal INP constituted of an I element and a Q element is multiplied by a frequency correlation signal FCORR generated from an oscillator NCO in a multiplier M, transformed into a frequency area in a fast Fourier transforming means FFT, and an output signal OU constituted of the I element and the Q element is formed. A mode is accurately identified, a sample window is almost accurately arranged, and coarse AFC is executed by a coarse AFC means CAFC. An intended continuous pilot signal CPIL of a present symbol in a data frame is extracted from an output signal from the FFT, and correlated in the CAFC according to defined layout.

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**Classes/Indexing****IPC**

IPC Code(1-7) **H04J 11/00** H04L 27/38 H04L 27/22

(6)

Current IPC-R	Invention	Version	Additional	Version
Advanced	H04L 27/38	20060101	H04H 20/72	20080101
	H04J 11/00	20060101		
	H04L 27/00	20060101		
	H04L 27/22	20060101		
	H04L 27/26	20060101		
	H04N 7/24	20060101		

Core	H04L 27/38	20060101		
	H04J 11/00	20060101		
	H04L 27/00	20060101		
	H04L 27/22	20060101	-	-
	H04L 27/26	20060101		
	H04N 7/24	20060101		
Subclass	-	-	-	-

**ECLA**



H04N000724A H04L002726M5C3 H04L002726M5C5 T04H002072

**DWPI Manual Codes** Expand DWPI Manual Codes**Legal Status****INPADOC Legal Status**

Get Family Legal Status

**Family****Family** Expand INPADOC Family (11)**Claims**

No Claims exist for this Record

**Description****Drawing Description** Expand Drawing Description**Description** Expand Description**Citations****Citation**

Citing Patents (0)

 Expand Cited Patents (7)

Cited Non-patents (0)

**Other**

No Other exists for this Record

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**JP11074863A Patent Images**

Image 1 of 1

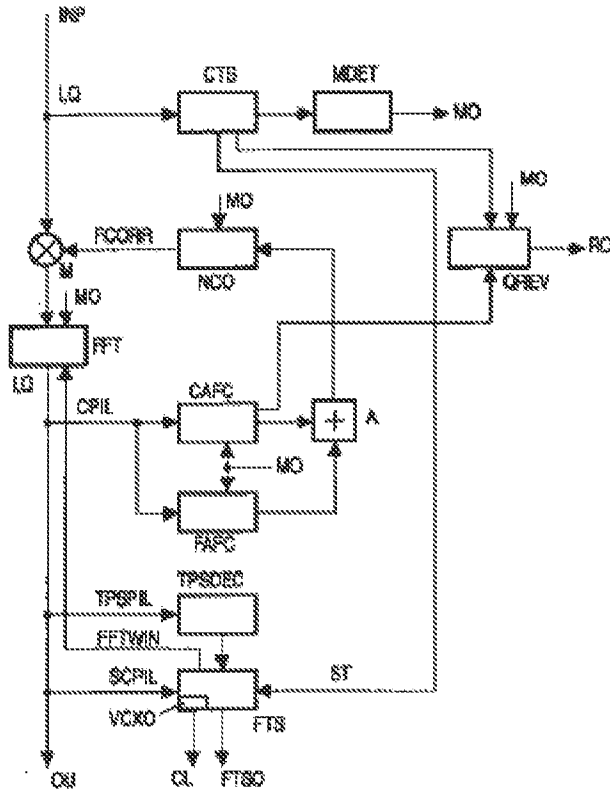


Fig.1